



# Electrical and Computer Engineering EE236 Microprocessor Systems Laboratory John J. Helferty



## Syllabus

**Instructor:** Dr. John Helferty

**TA:** Salvatore Giorgi  
salvatore.giorgi@temple.edu  
(267) 312-3734  
Office: 703d  
Office Hours: Wed 1:00 – 2:00  
Friday 1:00 – 2:00  
Or by appointment

**Textbook:** Supplied Materials and Handouts

**Prerequisites:** EE 2612 Digital Circuit Design

**Co-requisite:** EE 3612 Microprocessor Systems

### Expectations:

1. Be on time.
2. Be respectful to your classmates.
3. Be prepared for lab by paying attention in class.

### Lab Grades:

Lab Report Average: 90%

Final: 10%

A: 100 – 90, B: 89-80, C: 79-70, D: 69 - 60, F: 59 – 0

If you have a problem with a grade, please come see me during office hours or make an appointment.

### Lab Reports:

Lab reports must follow the set format shown below:

1. Cover page

2. Introduction
3. Formal Problem Statement
4. Procedure
5. Discussion
6. Conclusion
7. References
8. Appendix

### **Turning in Lab Reports:**

1. You have a maximum of one week to turn in your lab report, unless stated otherwise.
2. Lab reports must be emailed to me (PDF or Word doc). Include class number, section number, and lab number in the title of your email (ex. EE 3613 Section 001 Microprocessors Lab 1).
3. You must email reports to me BEFORE your next scheduled lab time. For example, if your lab starts at 2:00 pm, the time on your email must be 1:59 pm or earlier. Anything later than that will be considered late.
4. 10 points are deducted from any report turned in within 24 hours after the due date. An additional 10 points are deducted each subsequent 24 hour period after the due date.
5. Any labs turned in later than 3 days after the due date will receive an automatic 0.
6. Special consideration will be given in the case of a documented emergency.

### **Schedule:**

- Lab 1: Soda Dispenser
- Lab 2: Intro to Logicflex
- Lab 3: Data Movement Instructions
- Lab 4: Stack Operations
- Lab 5: Subroutine Call and Return
- Lab 6: Arithmetic Functions
- Lab 7: Intro to Digiview and Flashlite
- Lab 8: I/O Port Manipulation via x86 Assembly
- Lab 9: I/O Port Manipulation via C
- Lab 10: Logicflex BIOS Real Time Clock Functions
- Lab 11: Seven Segment Display with Logicflex